

<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>		Application No.	09/724,585
		Filing Date	November 28, 2000
		First Named Inventor	John Mark Beardslee
		Art Unit	2123
		Examiner Name	Thangavelu, K.
Total Number of Pages in This Submission	18	Attorney Docket Number	2986P035 /

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	James C. Scheller, Reg. No. 31,195 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
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Date	2/1/2005

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Attorney's Docket No. 02986.P035

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:	)	
	)	
John Mark Beardslee, et al.	)	Examiner: Kandasamy Thangavelu
	)	
Application No.: 09/724,585	)	Art Unit: 2123
	)	
Filed: November 28, 2000	)	
	)	Confirmation No.: 7620
For: Method And Systems For	)	
Debugging An Electronic	)	
System	)	
<hr/>		
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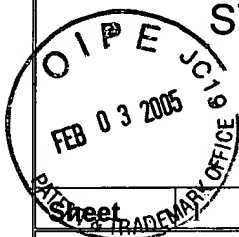
  
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

**Complete if Known**

Application Number	09/724,585
Filing Date	11-28-2000
First Named Inventor:	Beardslee, et al.
Art Unit	2123
Examiner Name	Kandasamy Thangavelu
Attorney Docket Number	02986.P035

of

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**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (If known)				
		US-	5,416,919	05-16-1995	Ogino et al.	
		US-	5,544,311	01-21-1997	Harenberg et al.	
		US-	5,596,743	08-06-1996	Bhat et al.	
		US-	5,771,240	05-12-1998	Tobin, et al.	
		US-	5,870,308	02-09-1999	Dangelo, et al.	
		US-	5,933,356	08-31-1999	Rostroker, et al.	
		US-	5,951,696	09-14-1999	Naaseh, et al.	
		US-	6,134,707	10-17-1996	Herrmann et al.	
		US-	6,175,946 B1	01-16-2001	Ly et al.	
		US-	6,191,683 B1	02-20-2001	Nygaard, Jr.	
		US-	6,199,031 B1	03-06-2001	Challier et al.	
		US-	6,202,044 B1	03-13-2001	Tzori	
		US-	6,202,172 B1	03-13-2001	Ponte	
		US-	6,223,148 B1	04-24-2001	Stewart, et al.	
		US-	6,255,836 B1	07-03-2001	Schwarz et al.	
		US-	6,255,845 B1	07-03-2001	Wong et al.	
		US-	6,263,373 B1	07-17-2001	Cromer et al.	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
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		Number-Kind Code <sup>2</sup> (If known)				
		US-	6,292,765 B1	09-18-2001	Ho et al.	
		US-	6,301,688 B1	10-09-2001	Roy	
		US-	6,311,292 B1	10-30-2001	Choquette et al.	
		US-	6,363,520 B1	03-26-2002	Boubezari et al.	
		US-	6,374,370 B1	04-16-2002	Bockhaus et al.	
		US-	6,377,911 B1	04-23-2002	Sample et al.	
		US-	6,377,912 B1	04-23-2002	Sample et al.	
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		US-	6,389,558 B1	05-14-2002	Herrmann et al.	
		US-	6,421,813 B1	07-16-2002	Jeddeloh	
		US-	6,425,101 B1	07-23-2002	Garreau	
		US-	6,430,727 B1	08-06-2002	Warren	
		US-	6,434,735 B1	08-13-2002	Watkins	
		US-	6,456,101 B2	09-24-2002	Dumbri	
		US-	6,456,961 B1	09-24-2002	Patil, et al.	
		US-	6,460,148 B2	10-01-2002	Veenstra et al.	
		US-	6,460,174 B1	10-01-2002	Carey	
		US-	6,463,392 B1	10-08-2002	Nygaard, et al.	
		US-	6,484,273 B1	11-19-2002	Chang	
		US-	6,493,852 B1	12-10-2002	Narain et al.	
		US-	6,499,123 B1	12-24-2002	McFarland et al.	
		US-	6,510,534 B1	01-21-2003	Nadeau-Dostie, et al.	
		US-	6,513,143 B1	01-21-2003	Bloom, et al.	
		US-	6,564,347 B1	05-13-2003	Mates	
		US-	6,567,932 B2	05-20-2003	Edwards, et al.	
		US-	6,571,751 B1	06-01-2004	Murray, et al.	
		US-	6,581,191 B1	06-17-2003	Schubert, et al.	

Examiner Signature		Date Considered	
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Attorney Docket Number	02986.P035

Sheet 4 of 4

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		HAUFE, J., et al., "Ein Debugger fuer ASIC-Prototypen", DASS Dresden Germany, 2000, pp., 10.	
		KIROVSKI, D., et al., "Improving the Observability and Controllability of Datapaths for Emulation-Based Debugging", IEEE Trans. on CAD, Vol. 18, November 1999, pp., 1529-1541.	
		VERMEULEN, B., et al., "Silicon debug of a co-processor array for video applications", pp. 6, High-Level Design Validation and Test Workshop, 2000, IEEE Proceedings.	
		VAN ROOTSELARR, G., et al., "Silicon debug: scan chains alone are not enough", pp. 11, ITC 1999.	
		VAN ROOTSELAAR, G., et. al., "Silicon Debug Methodology", pp. 32, Embedded Tutorial, ICCAD, 1999.	

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